



IPW

1011-67625-01 329437

PATENT
Attorney Reference No. 1011-67625-01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Rajski et al.

Application No. 10/777,443

Filed: February 10, 2004

Confirmation No. 4800

For: ARITHMETIC BUILT-IN SELF-TEST OF
MULTIPLE SCAN-BASED INTEGRATED
CIRCUITS

Examiner: Norman M. Wright

Art Unit: 2134

Attorney Reference No. 1011-67625-01

CERTIFICATE OF MAILING

I hereby certify that this paper and the documents referred to as being attached or enclosed herewith are being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: OFFICE OF INITIAL PATENT EXAMINATION CUSTOMER SERVICE CENTER, COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VA 22313-1450 on the date shown below.

Attorney
for Applicant(s)

Date Mailed November 19, 2004

REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT

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Applicants have received the official Filing Receipt for the application referenced above, a copy of which is attached as Exhibit A.

The following error appears on the Filing Receipt:

ITEM IN ERROR	CORRECT INFORMATION
Domestic Priority data as claimed by applicant	<u>This application is a continuation of U.S. Patent Application number 09/276,474, filed March 25, 1999, which is a continuation of U.S. Patent Application 08/814,042, filed on March 10, 1997, now U.S. Patent number 5,991,898, issued November 23, 1999.</u>

Exhibit A and B are attached for verification of the requested correction. Exhibit B is a copy of the Preliminary Amendment as filed on February 10, 2004, showing the complete domestic priority

data as claimed by Applicant.

Applicant requests that the identified error(s) be corrected and that a new official Filing Receipt be issued.

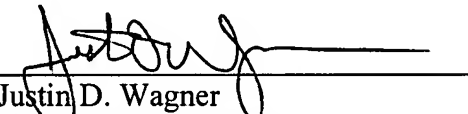
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Respectfully submitted,

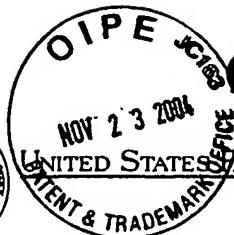
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By


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cc: Docketing



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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/777,443	✓ 02/10/2004	✓ 2829	✓ 806	1011-67625	✓ 8	✓ 22	✓ 3

CONFIRMATION NO. 4800 ✓

24197 ✓
KLARQUIST SPARKMAN, LLP
121 SW SALMON STREET
SUITE 1600
PORTLAND, OR 97204

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OC000000012847886

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Date Mailed: 06/02/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Janusz Rajski, West Linn, OR; ✓
Jerzy Tyszer, Poznan, POLAND; ✓

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 05/19/2004 ✓

Projected Publication Date: 08/11/2005 ✓

Non-Publication Request: No ✓

Early Publication Request: No ✓

Title

Arithmetic built-in self-test of multiple scan-based integrated circuits ✓

Preliminary Class

EXHIBIT

tabbles

A

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